



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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6/4/03

Applicants: Daniel L. Auclair et al.  
Title: Soft Errors Handling in EEprom Devices  
Application No.: 08/908,265 Filing Date: August 7, 1997  
Examiner: Moise, Emmanuel Lionel Group Art Unit: 2133  
Docket No.: SNDK.026US3 (formerly M-10202-3C US) Conf. No.: 9300

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*Gulien Bow*

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Dear Sir:

The applicants respond to the to the Official Action mailed on May 5, 2003, as follows:

In the Claims:

Please amend claim 36 as indicated:

D

36.(Amended) The memory device of claim 35, wherein a group of the plurality of memory cells are arranged in a row that includes the first cell, the memory device further comprising means for rewriting a previously stored value into each of the group of memory cells when the means for determining determines that the first cell has a degraded state.

Please amend claims 45-47 as indicated:

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